

## WEST Search History





DATE: Tuesday, October 25, 2005

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L15	L14 not (L7 or L13 or L11)	375
<input type="checkbox"/>	L14	((silicide or salicide) with plug with (interconnect or (bit adj line) or wiring))	454
<input type="checkbox"/>	L13	L12 and floating	36
<input type="checkbox"/>	L12	L11 and memory	225
<input type="checkbox"/>	L11	L10 and ((upper or upmost or upward or top) with plug)	243
<input type="checkbox"/>	L10	L9 and ((salicide or silicide) with plug)	462
<input type="checkbox"/>	L9	L8 and (drain and channel)	1297
<input type="checkbox"/>	L8	((polysilicon or poly or polycrystalline) with plug) and (silicide or salicide)	3443
<input type="checkbox"/>	L7	L6 and channel and drain	119
<input type="checkbox"/>	L6	((silicide or salicide) with (top or upper) with plug)	337
	<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L5	((polysilicon or (polycrystalline adj silicon)) with (plug or via or stud) same (source and drain))	495
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L4	L2 and leakage	240
<input type="checkbox"/>	L3	L2 and memory and (floating adj body)	2
<input type="checkbox"/>	L2	L1 and ((silicide or salicide) same (plug or via or stud))	695
<input type="checkbox"/>	L1	((polysilicon or (polycrystalline adj silicon)) with (plug or via or stud) same (source and drain))	2222

END OF SEARCH HISTORY

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		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L8	L7 not (L5 or L2 or L3)	832
<input type="checkbox"/>	L7	channel and source and drain and gate and ((silicide or salicide) with plug)	856
<input type="checkbox"/>	L6	channel and source and drain and gate and((silicide or salicide) with plug)	856
<input type="checkbox"/>	L5	L4 not (L2 or L3)	476
<input type="checkbox"/>	L4	(salicide or silicide) and (floating adj body)	493
<input type="checkbox"/>	L3	(salicide or silicide) and (floating adj body adj cell)	16
<input type="checkbox"/>	L2	L1 and memory	242
<input type="checkbox"/>	L1	fBC or (floating adj body adj cell)	1069

END OF SEARCH HISTORY